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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of

Guy M. Cohen et al.

Serial No.: 09/902,483

Filing Date: July 11, 2001

Group Art Unit: 2813

Examiner: Erik J. Kielin

For: SELF-ALIGNED SILICIDE (SALICIDE) PROCESS FOR LOW RESISTIVITY  
CONTACTS TO THIN FILM SILICON-ON-INSULATOR AND BULK MOSFETS  
AND FOR SHALLOW JUNCTIONS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. In compliance with the concise explanation requirement under 37 CFR §1.98(a)(3) for foreign language, an English-language Abstract is attached to one of the foreign language references and English language equivalents are attached.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware. I hereby certify that each item of information contained in this Information Disclosure Statement was the first citation of that item by a foreign patent office in a counterpart foreign application, which occurred not more than three months prior to the filing of this statement.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Assignee's Deposit Account No. 50-0510.

Respectfully submitted,

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